

FIG. 1

and FIG. 2 illustrates the system architecture.

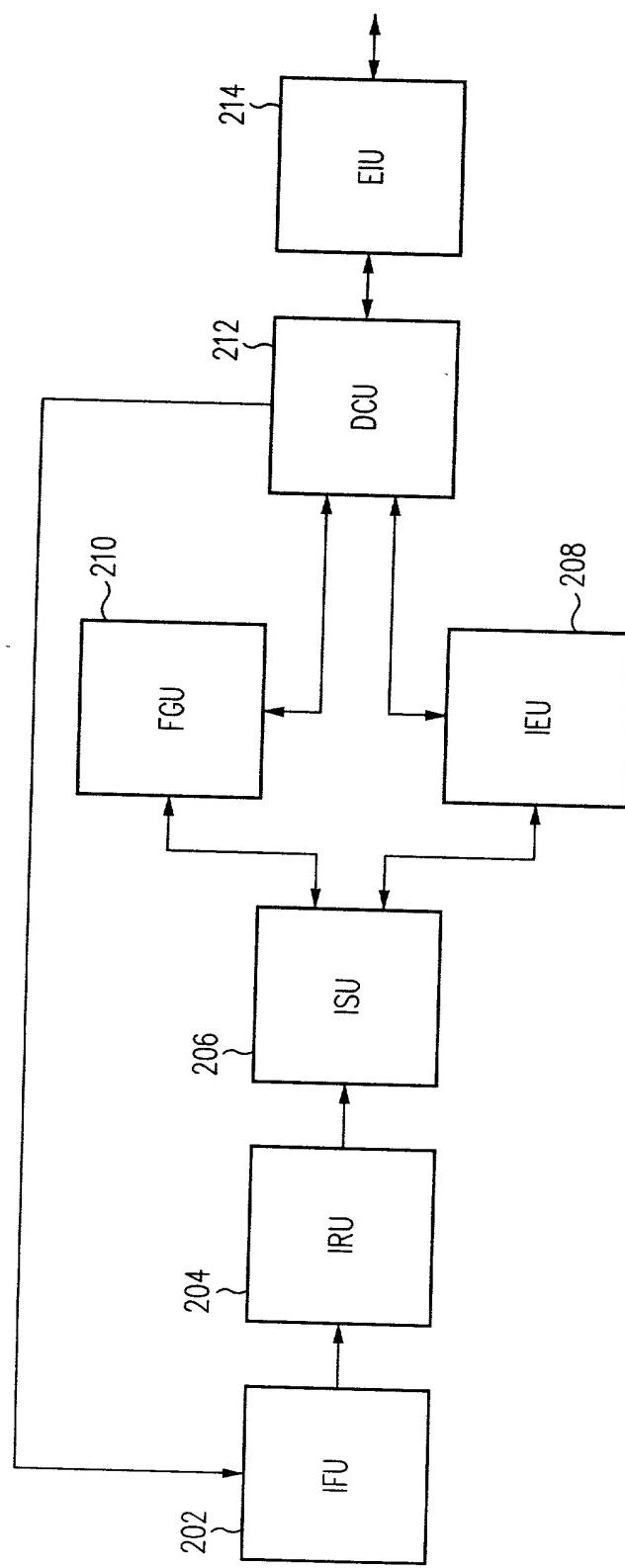


FIG. 2

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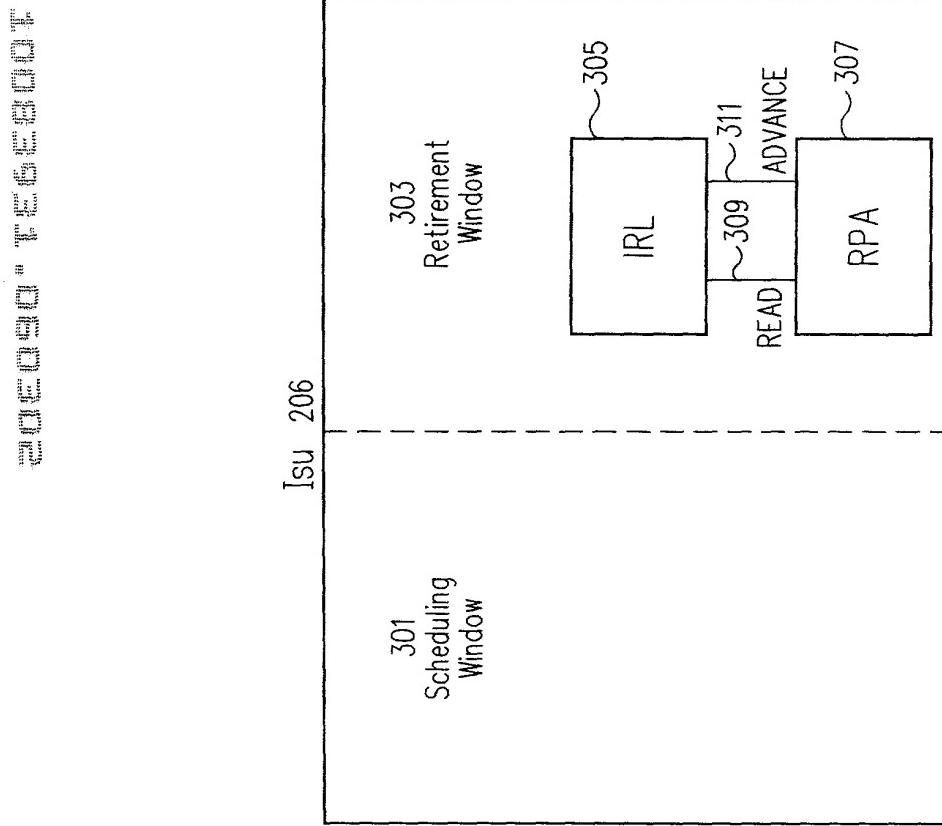


FIG. 3

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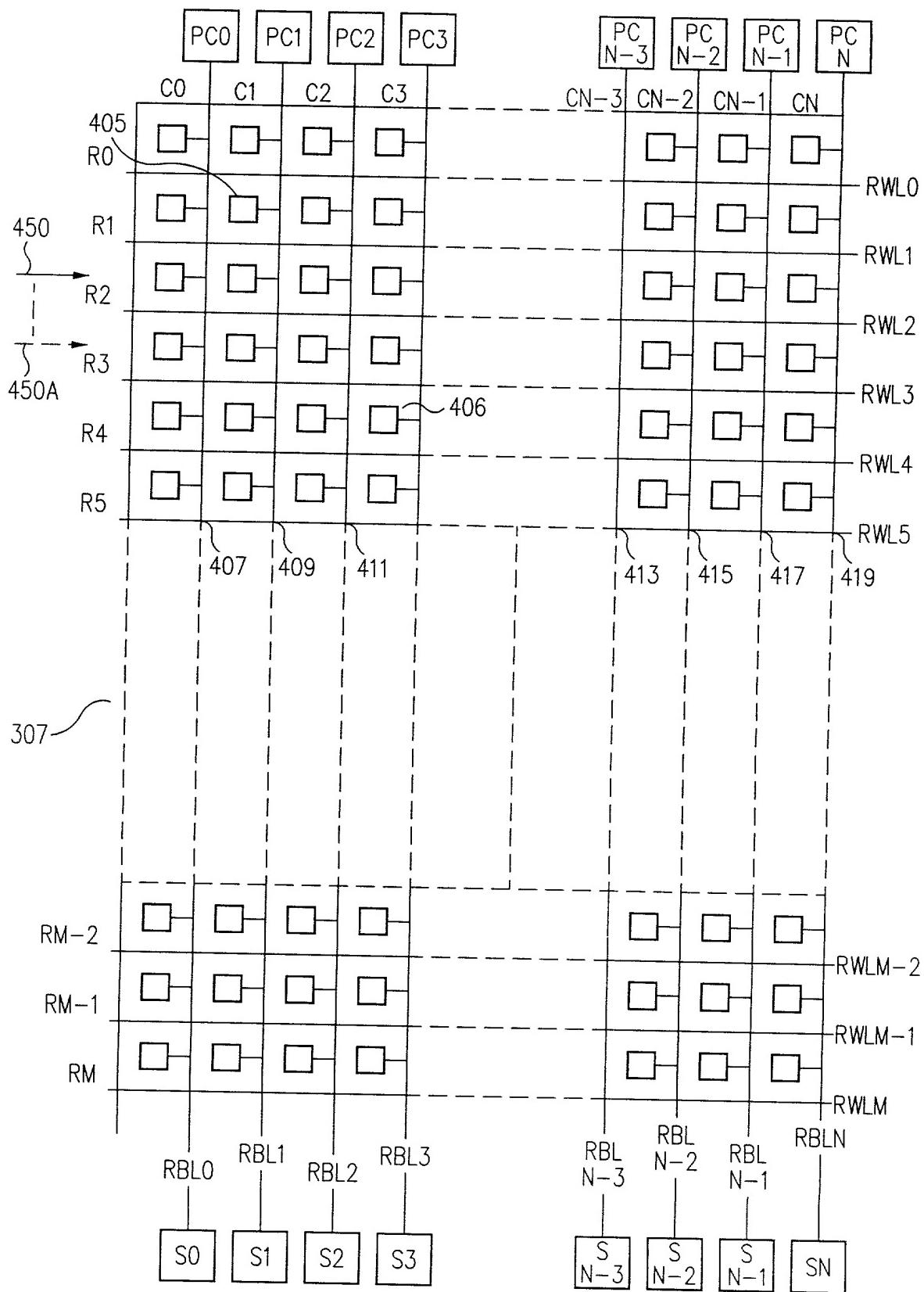


FIG. 4

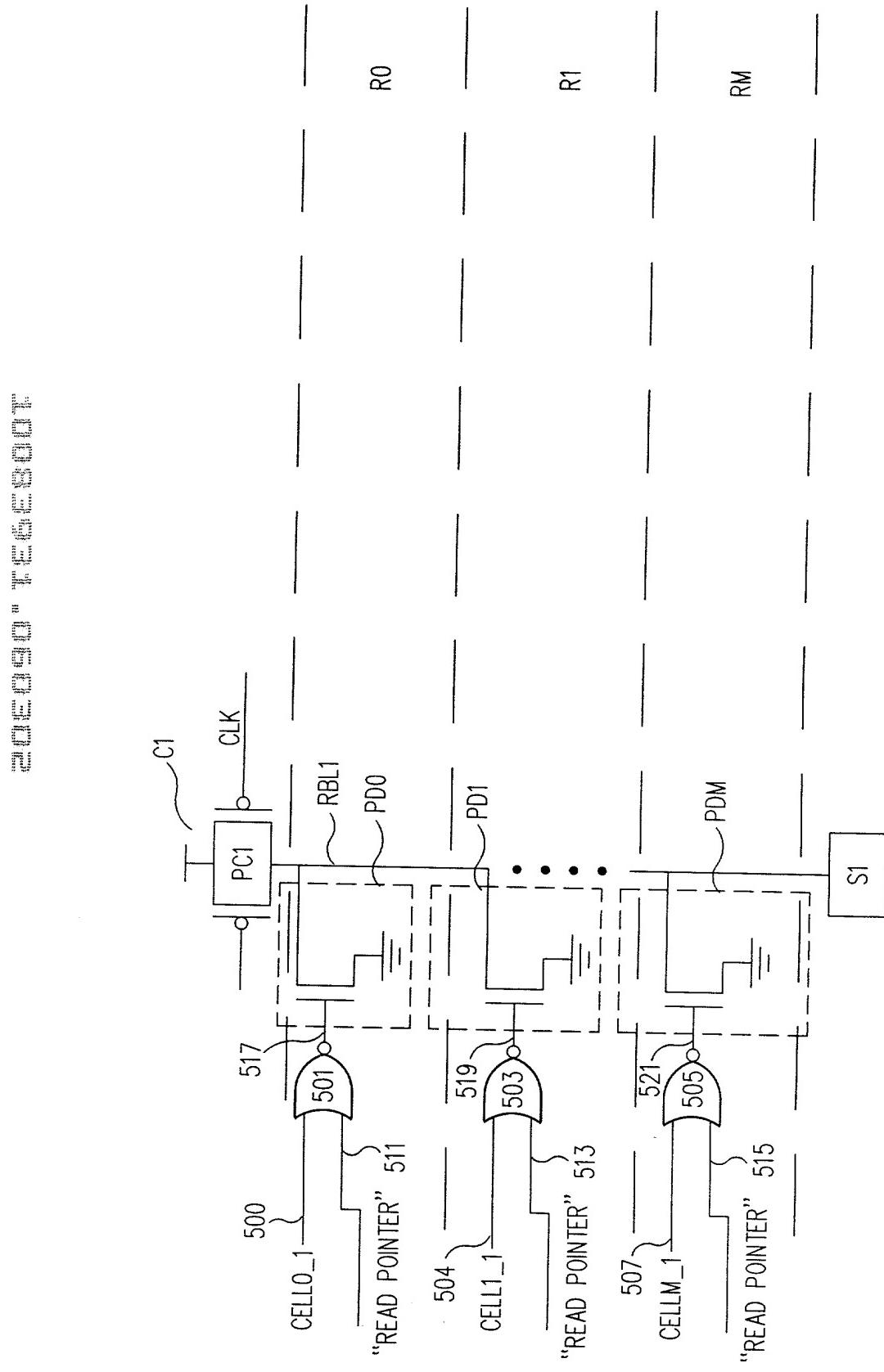


FIG. 5
(PRIOR ART)

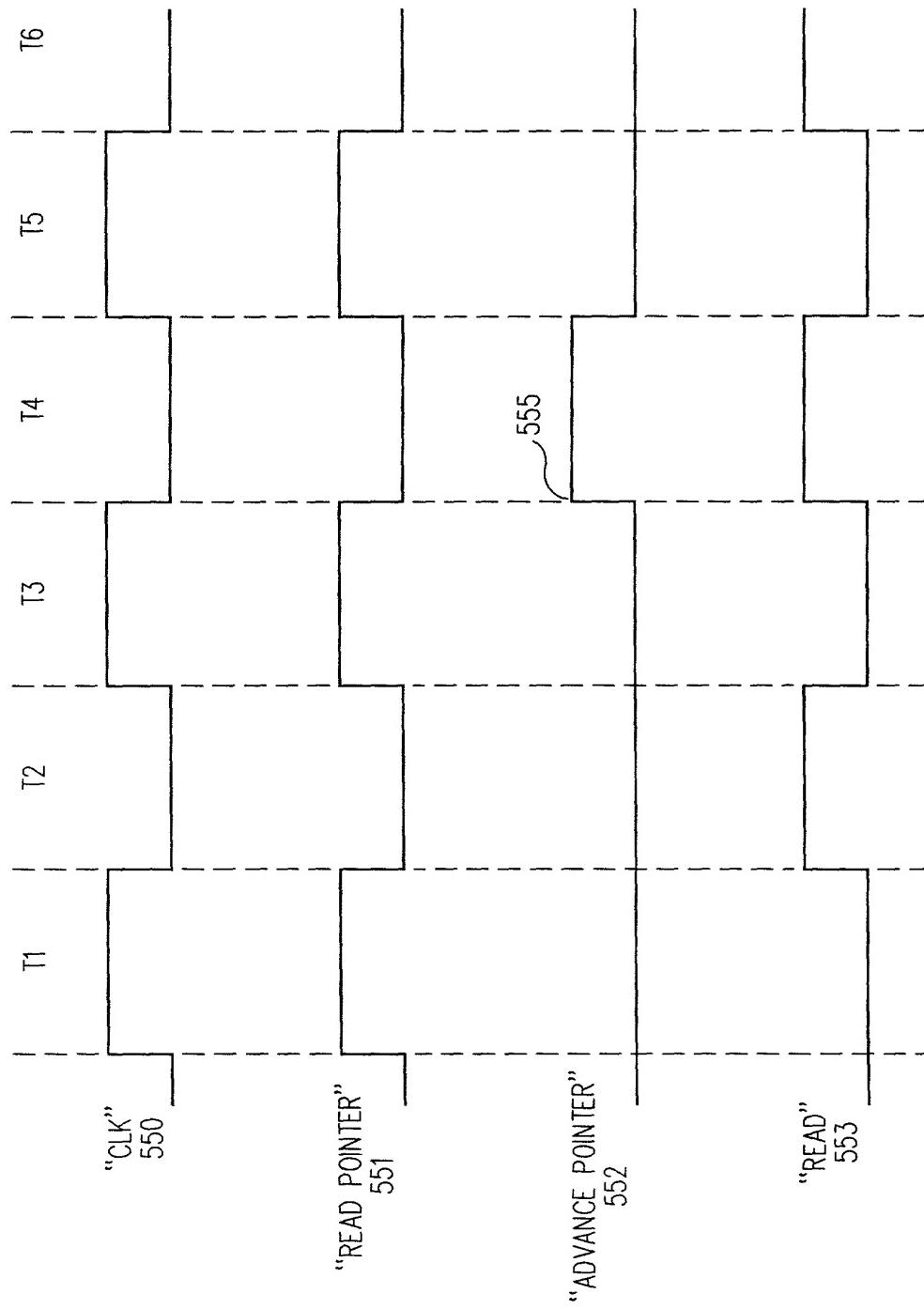


FIG. 5A
(PRIOR ART)

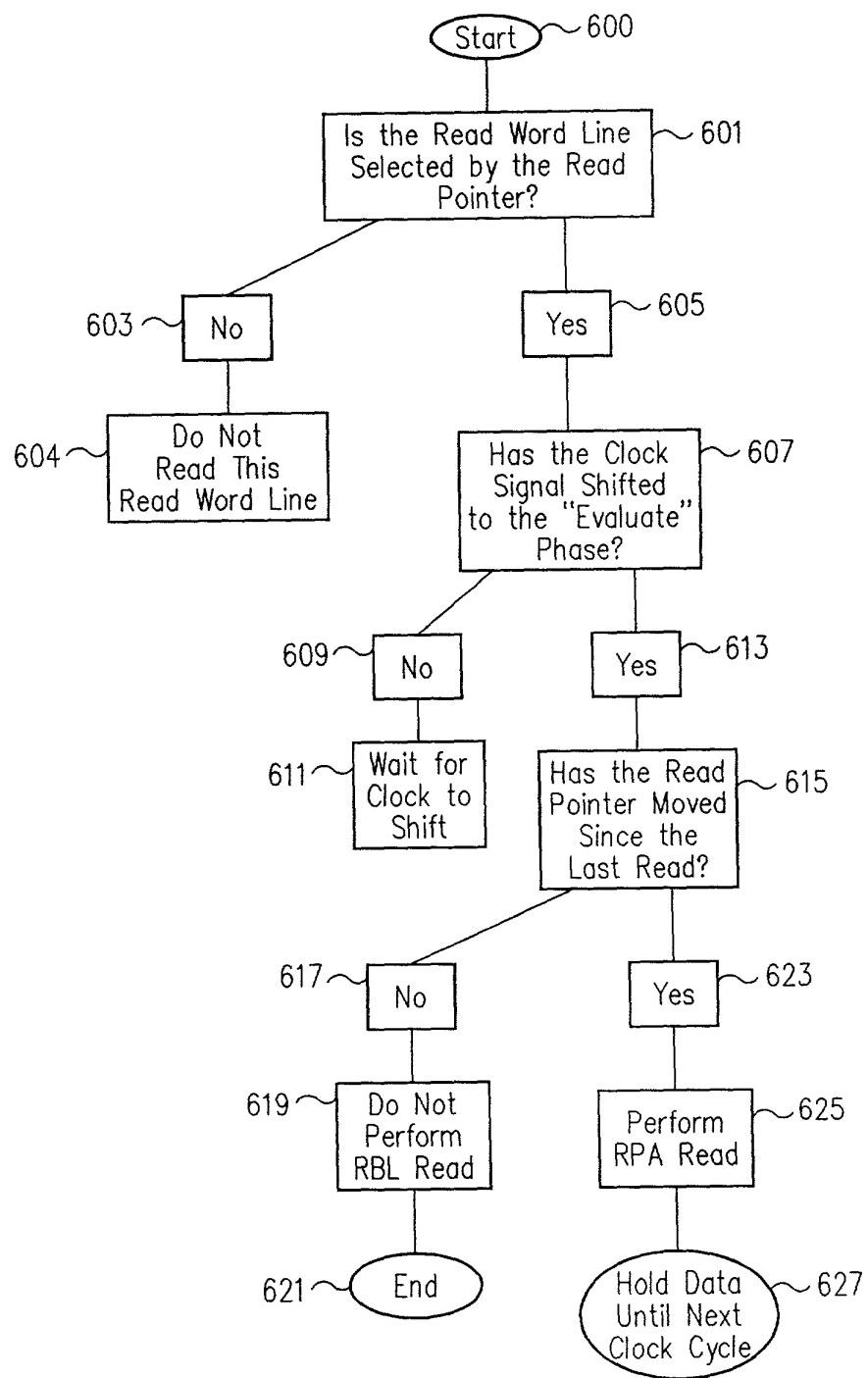


FIG. 6

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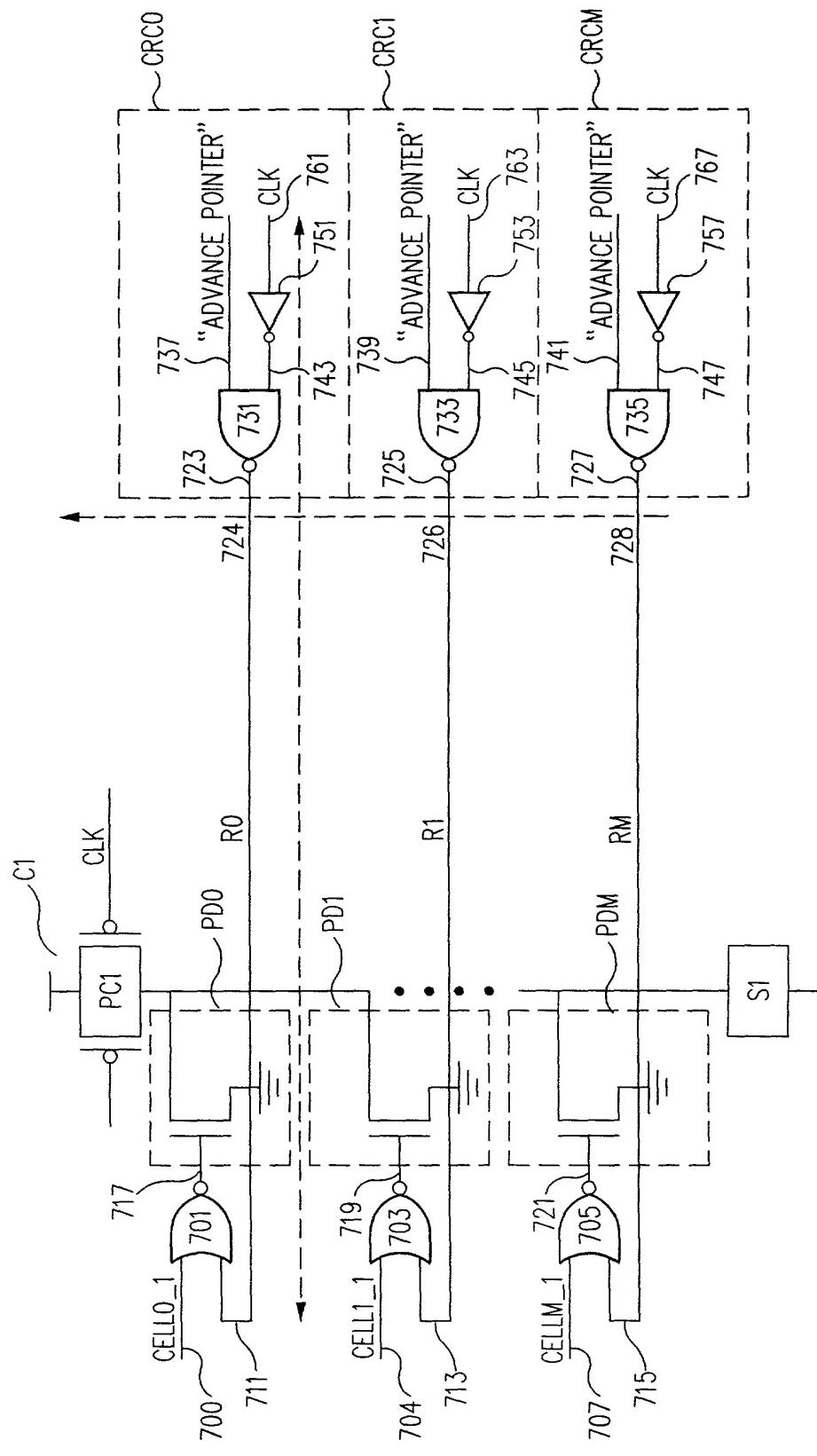


FIG. 7

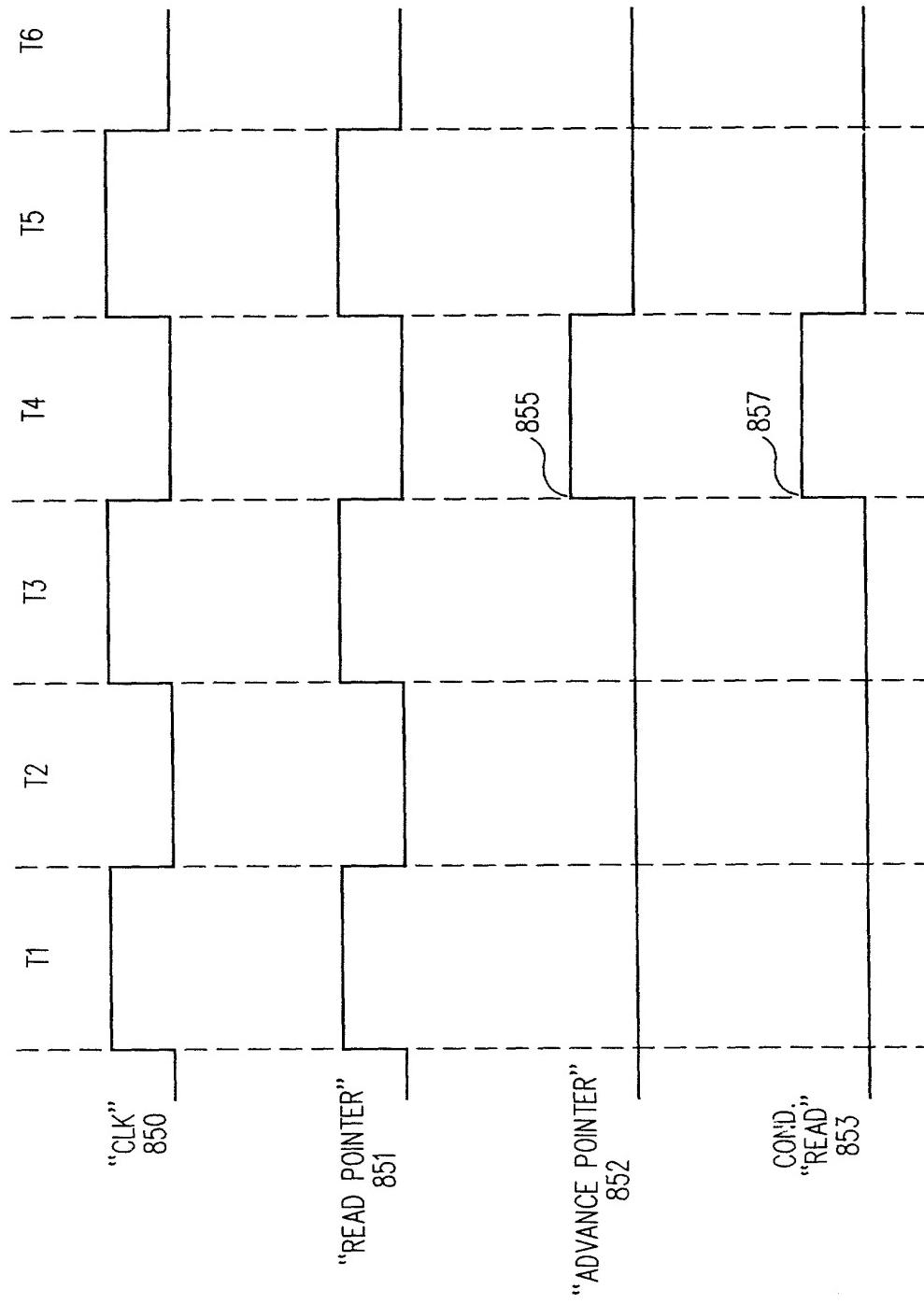


FIG. 8

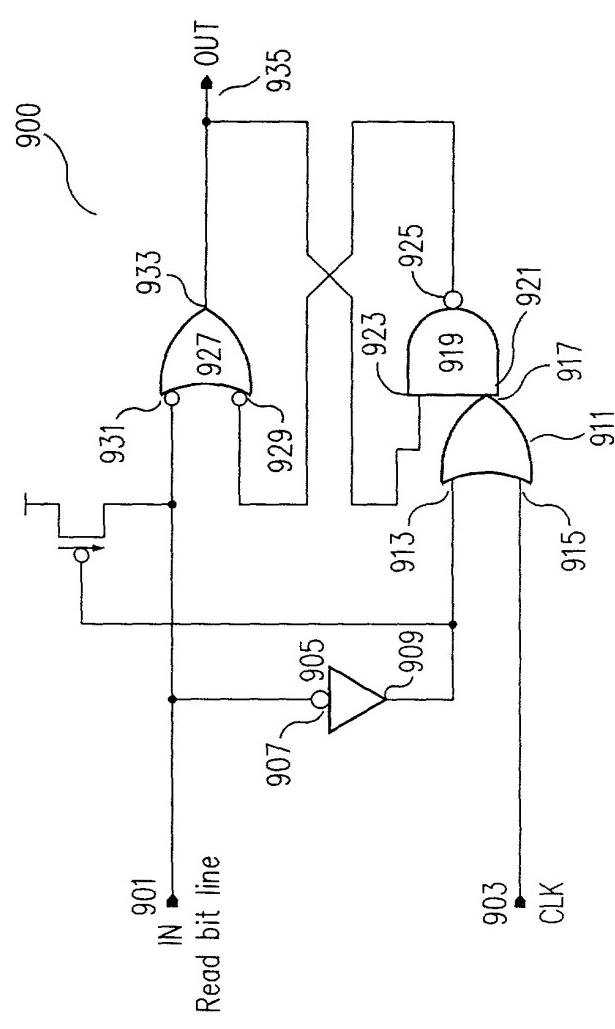


FIG. 9A
(PRIOR ART)

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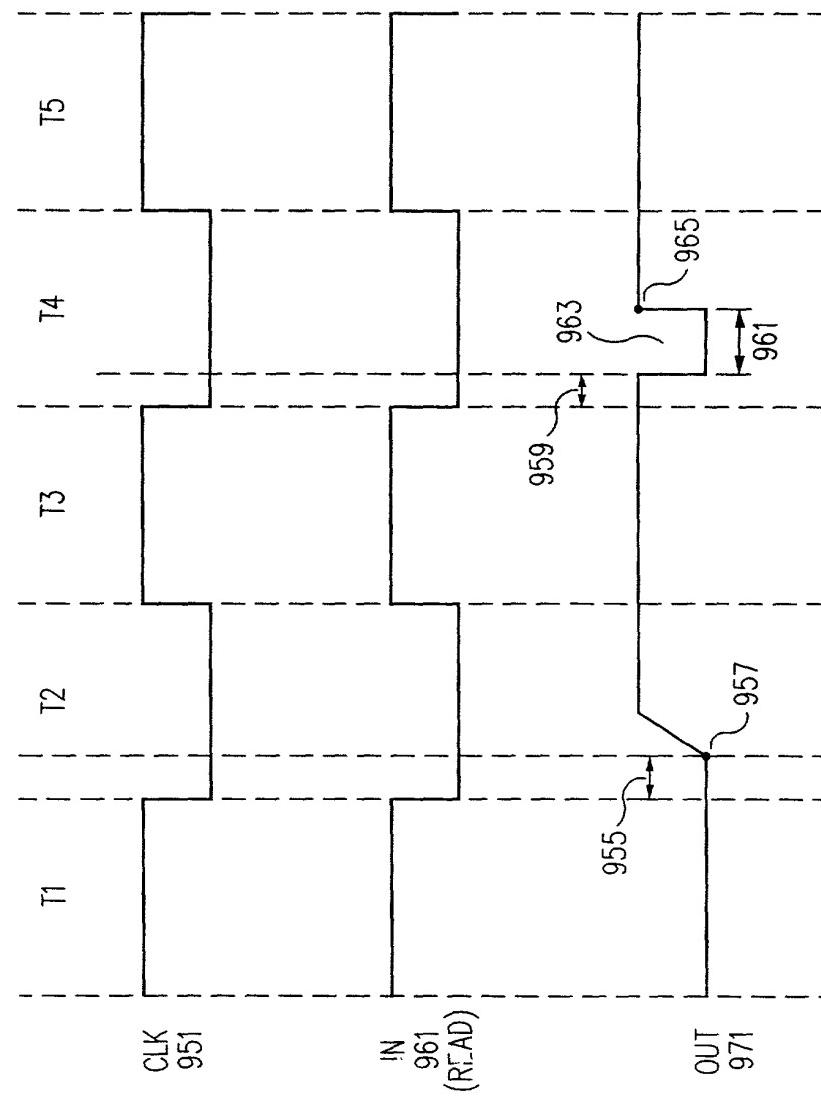


FIG. 9B
(PRIOR ART)

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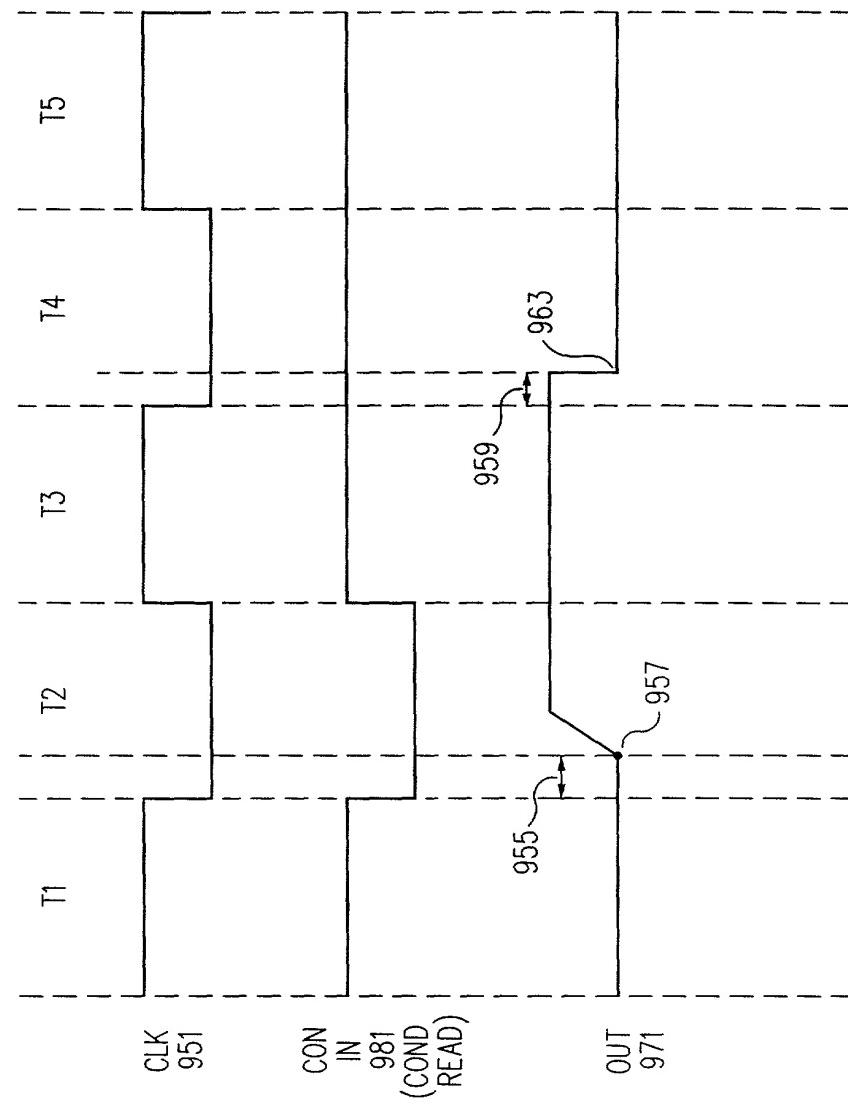


FIG. 9C
(PRIOR ART)

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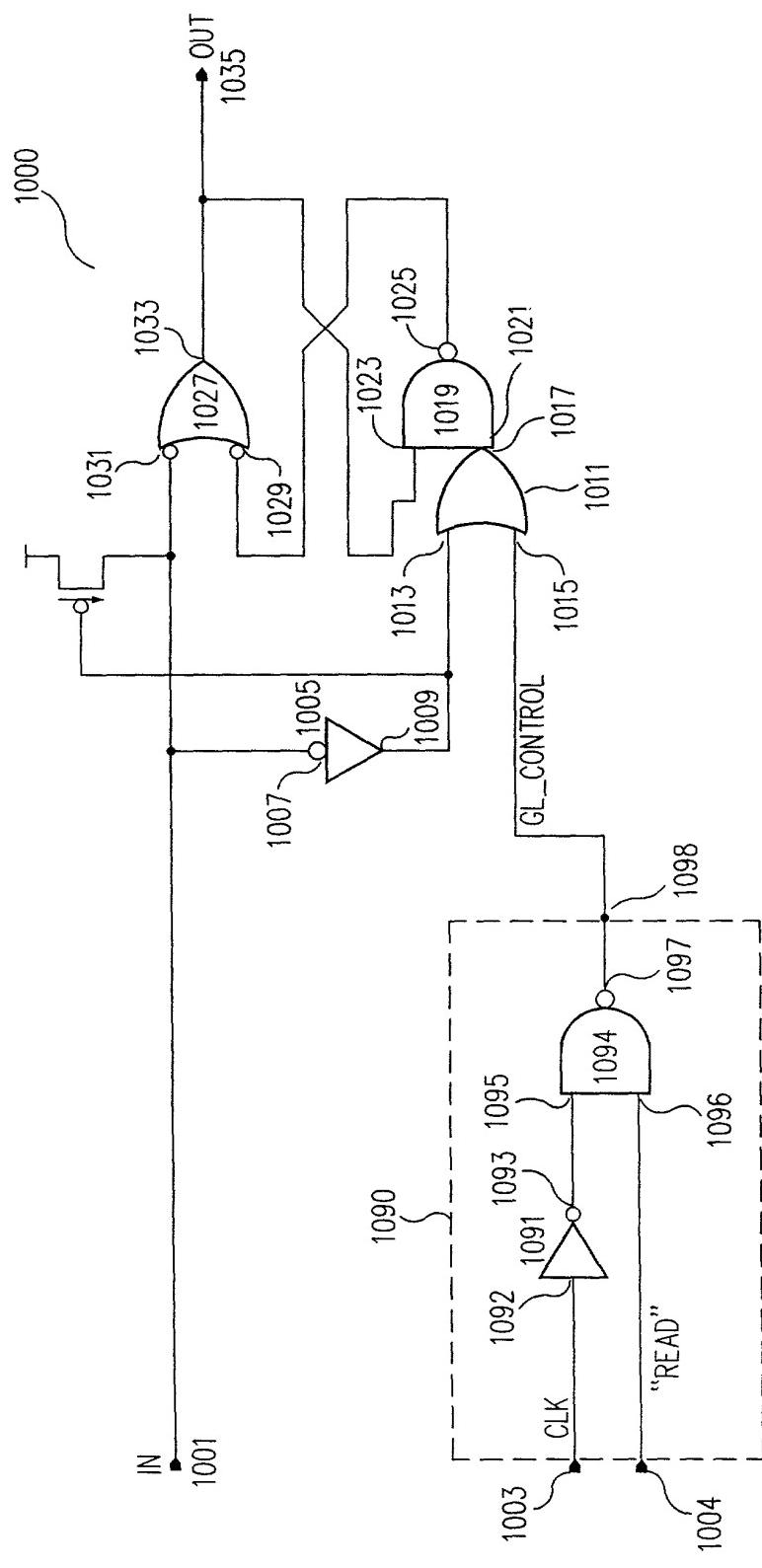


FIG. 10

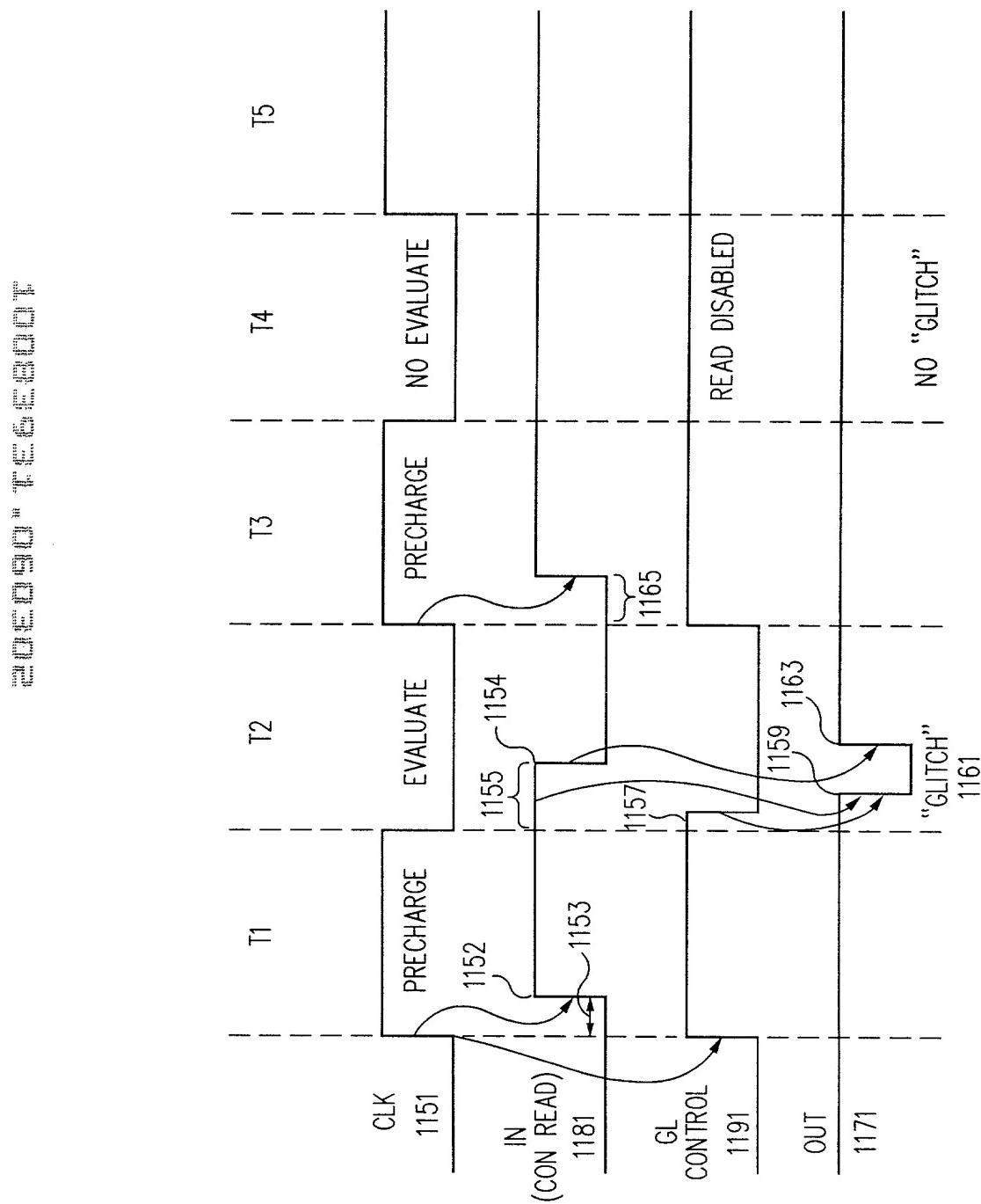


FIG. 11

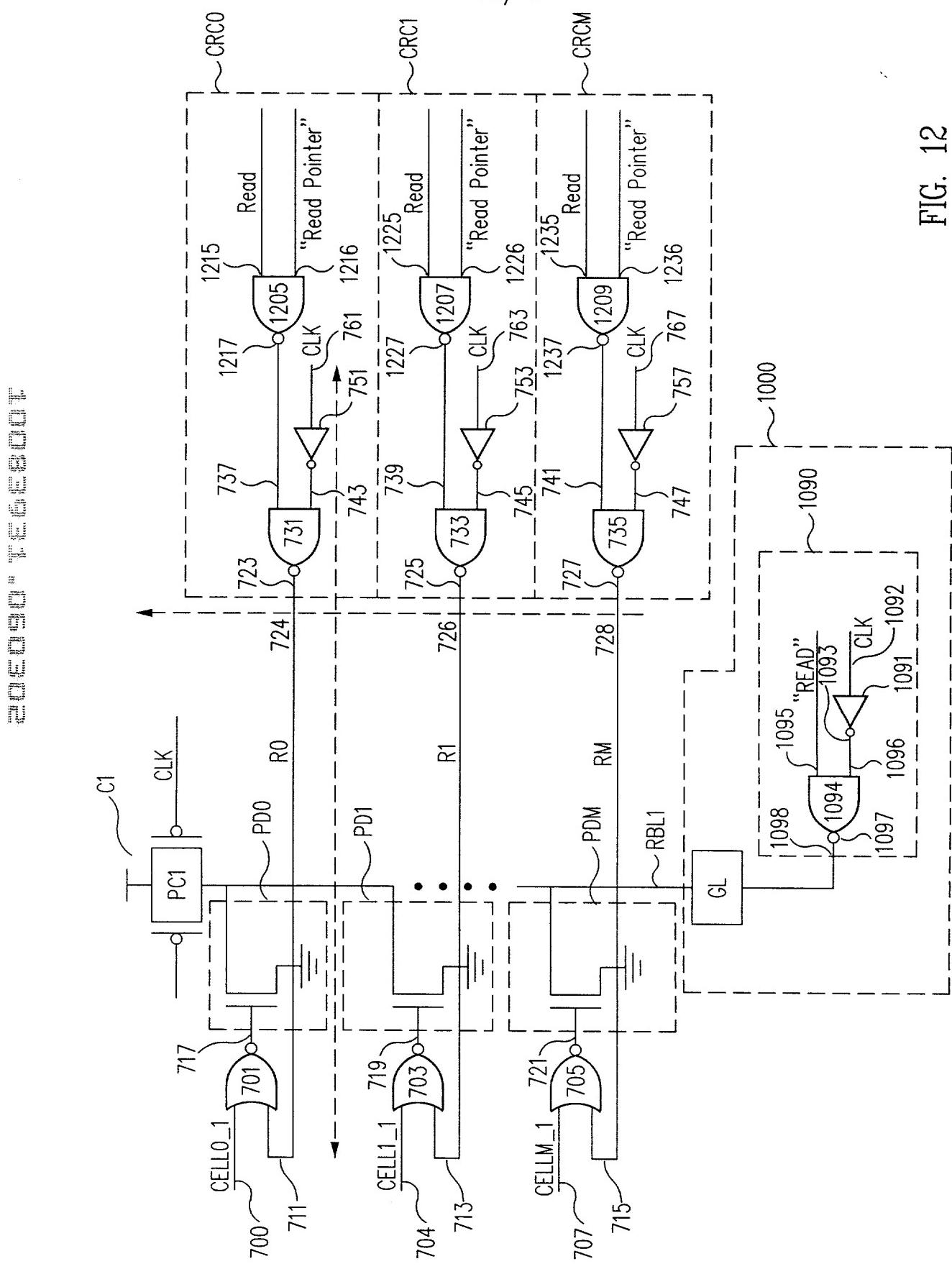


FIG. 12